MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE, MADANAPALLE

(UGC-AUTONOMOUS)

(Affiliated to JNTUA, Ananthapuramu & Approved by AICTE, New Delhi)

M.Tech I Year I Semester (R14) Supplementary End Semester Examinations - Feb 2018

Results - VLSI & Embedded Systems

The following is the provisional result of the candidates who appeared for the above Examination.

S.No	Registered No.	14VES11T04 ANALOG & DIGITAL IC DESIGN			CREDITS TAKEN	CREDITS EARNED	SGPA
		Credits	Letter Grade	Grade Point			
1	14691D6819	0	F	0	4	0	7.09

Controller of Examinations